

### BOARD DESCRIPTION

A populated AD8349 evaluation board is available.

The AD8349 has an exposed paddle underneath the package, which is soldered to the board. The evaluation board is designed without any components on the underside of the board so that heat may be applied under the AD8349 for easy removal and replacement of the DUT.

### ORDERING GUIDE

Model	Description
AD8349-EVAL	Evaluation Board

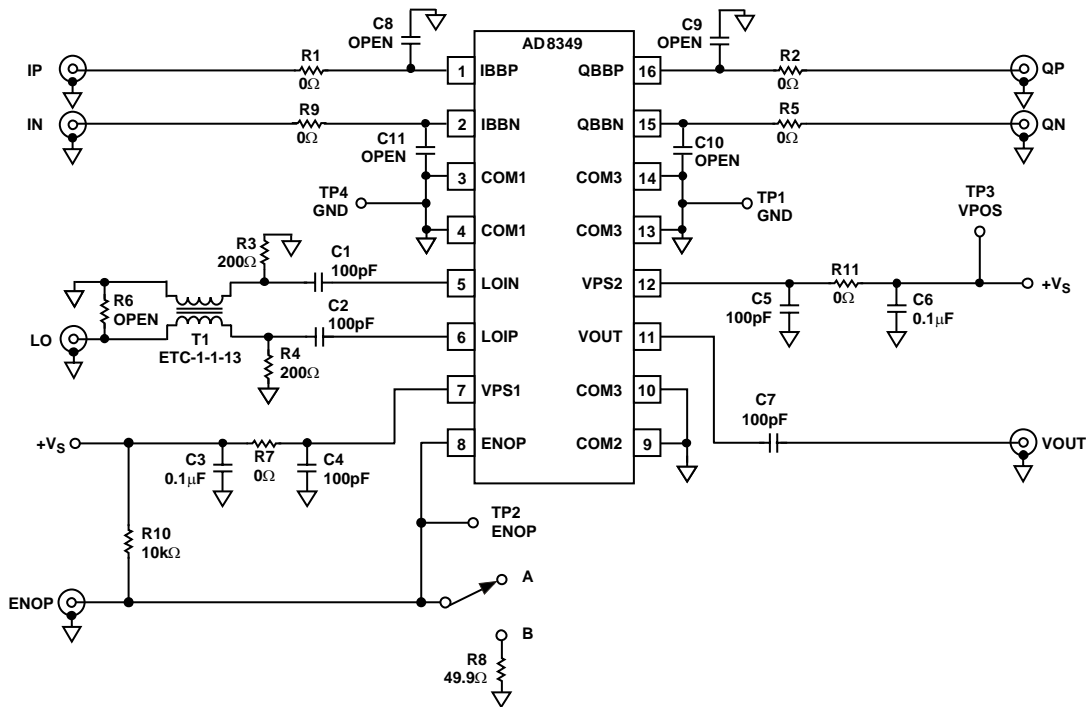


Figure 1. Evaluation Board Schematic

03570-0-072

### Rev. 0

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# EVAL-AD8349EB

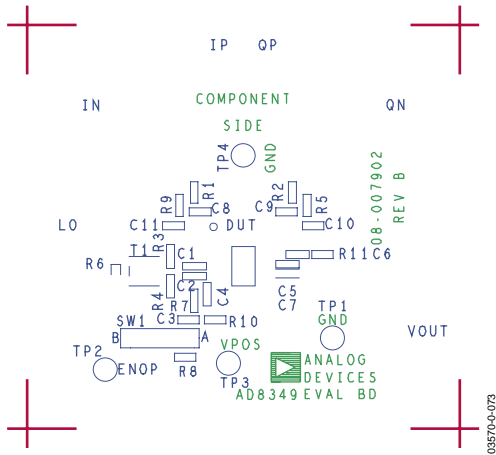


Figure 2. Evaluation Board Silkscreen

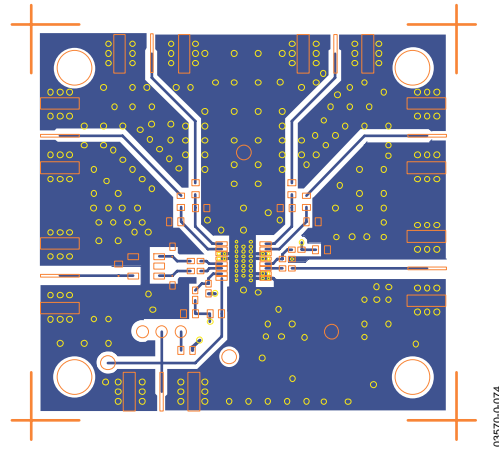


Figure 3. Layout of Evaluation Board, Top Layer

Table 1. Evaluation Board Configuration Options

Component	Function	Default Condition
TP1, TP4, TP3	Power Supply and Ground Vector Pins.	Not applicable
SW1, ENOP, TP2	Output Enable. Place in the A position to connect the ENOP pin to +V <sub>s</sub> via pull-up resistor R10. Place in the B position to disable the device by grounding the ENOP pin through a 49.9 Ω pull-down resistor. The device may be enabled via an external voltage applied to the SMA connector ENOP or TP2.	SW1 = A
R1, R2, R5, R9, C8–C11	Baseband Input Filters. These components can be used to implement a low-pass filter for the baseband signals.	R1, R2, R5, R9 = 0 Ω, C8–C11 = Open

## ESD CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

